	Type	L #	Hits	Search Text
1	BRS	L1	254048	transistor and ((field adj effect$3) or bipolar or (current near2 (control$3 driv$3)))
2	BRS	L2	1105	(fuse$1 fusible fus$3) adj2 link$3 and (isolat$3 (cut adj off)) with (current amp ampere volt$4)
3	BRS	L3	144292	(memor$3 same program$4) same memory with (array element device)
4	BRS	L4	160	2 and 3
5	BRS	L5	61015	"347"/$.ccls.
6	BRS	L6	67690	"365"/$.ccls.
7	BRS	L7	67	4 and (5 6)
8	BRS	L8	11177	(fuse$1 fusible fus$3) adj2 (densit$3 element)
9	BRS	L9	135382	(memor$3 with program$5) same memory with (array element device)
10	BRS	L10	103	1 and 2 and 9
11	BRS	L11	36	8 and 10
12	BRS	L12	24	11 and (5 6)
13	BRS	L13	453	(inkjet (ink adj jet)) near2 (printhead printer pen chip) same program$5 with memor$3
14	BRS	L14	0	12 and 13
15	BRS	L15	0	13 and 10
16	BRS	L16	173	13 and (5 6)
17	BRS	L17	1	16 and 8 and 9
18	BRS	L18	48	16 and (8 9)

	DBs	Time Stamp	Comments
1	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/08/09 12:59	
2	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/08/09 13:09	
3	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/08/09 13:10	
4	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/08/09 13:06	
5	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/08/09 13:08	
6	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/08/09 13:08	
7	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/08/09 13:09	
8	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/08/09 13:09	
9	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/08/09 13:11	
10	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/08/09 13:12	
11	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/08/09 13:12	
12	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/08/09 13:14	
13	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/08/09 13:17	
14	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/08/09 13:17	
15	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/08/09 14:05	
16	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/08/09 14:05	
17	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/08/09 14:06	
18	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/08/09 14:06	